

UP v. 2.51 - file: NUDV-83 - NORMAL.hex

File Edit View Device Options Help

Open... Save Main Data Configuration Program Read Verify Erase Blank check

Code memory

0000	3005	2148	2900	3FFF	00FF	0E03	1283	00FE	0E0A	00FD	118A	1283	1C70	2010	188C	292D	.H	..t.0Sp.\$-		
0010	1C0C	2817	300D	008F	300A	008E	14F0	18E8	0064	018C	01E8	0E7D	008A	0E7E	0083	0E7F	..v2.zdcaS8yS~			
0020	0E7F	0089	0782	3417	3411	3411	3413	3413	3413	3413	3415	3415	3415	3417	3417	3417	3417	
0030	3411	3413	3415	0782	3420	341E	3421	3424	341E	3421	3424	341E	3421	3424	3421	341E	3421	
0040	3424	3428	3428	3428	393F	0782	342A	342E	3432	3436	343A	343E	3441	3445	3448	344A	3448	344A	\$(\$(
0050	344D	344F	3451	3452	3453	3454	3454	3454	3453	3452	3451	344F	344D	344A	3448	3445	3448	3445	HO	QR
0060	3441	343E	343A	3436	3432	342E	342A	3426	3422	341E	341A	3416	3413	340F	340C	340A	3408	340A	A	>
0070	3407	3405	3403	3402	3401	3400	3400	3400	3401	3402	3403	3405	3407	340A	340C	340F	3408	340F
0080	3413	3416	341A	341E	3422	3427	393F	0782	342A	342D	3431	3434	3437	343A	343D	3440	3440	3440
0090	3442	3444	3446	3448	3449	3448	3448	344C	344C	3448	3448	3449	3448	3446	3444	3446	3444	3446	B	F
00A0	3442	3440	343D	343A	3437	3434	3431	342D	342A	3427	3423	3420	341D	341A	3417	3414	3412	3410	0	B
00B0	3412	3410	340E	340C	3408	3409	3409	3408	3408	3408	3409	3409	3408	340C	340E	3410	340E	3410
00C0	3412	3414	3417	341A	341D	3420	3423	3427	393F	0782	3435	3432	3402	3402	3402	3401	3401	3401
00D0	340C	340A	3418	3401	3401	3401	3400	3409	3400	3400	3405	3405	3406	3406	340A	340A	340A	340A
00E0	340A	340A	3408	3408	3401	340A	3405	3405	3405	3405	3406	3406	3401	3401	3401	3401	3401	3401
00F0	34FF	34FF	34FF	34FF	34FF	34FF	34FF	34FF	34FF	34FF	01FB	3093	2148	00FB	28FB	0080	34FF	34FF

PRESTO programmer settings

Idle power supply: None / External Actual voltage on VDD: **None**

Active power supply: Internal 5V

-MCLR pin control: Run Tristate HVP

Programming method: HVP

Stop Reset

Configuration

Fuses: CP CPD LVP BOREN

#PWRITE: Enabled

WRT: Writable WDTE

OSC: HS

ID: 3FFF 3FFF 3FFF 3FFF ...

EEPROM

00	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
10	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
20	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
30	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
40	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
50	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
60	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
70	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF

PRESTO programmer settings

Idle power supply: Internal 5V Actual voltage on VDD: **approx. 5V**

Active power supply: Internal 5V

-MCLR pin control: Run Tristate HVP

Programming method: HVP

Stop Reset

Configuration

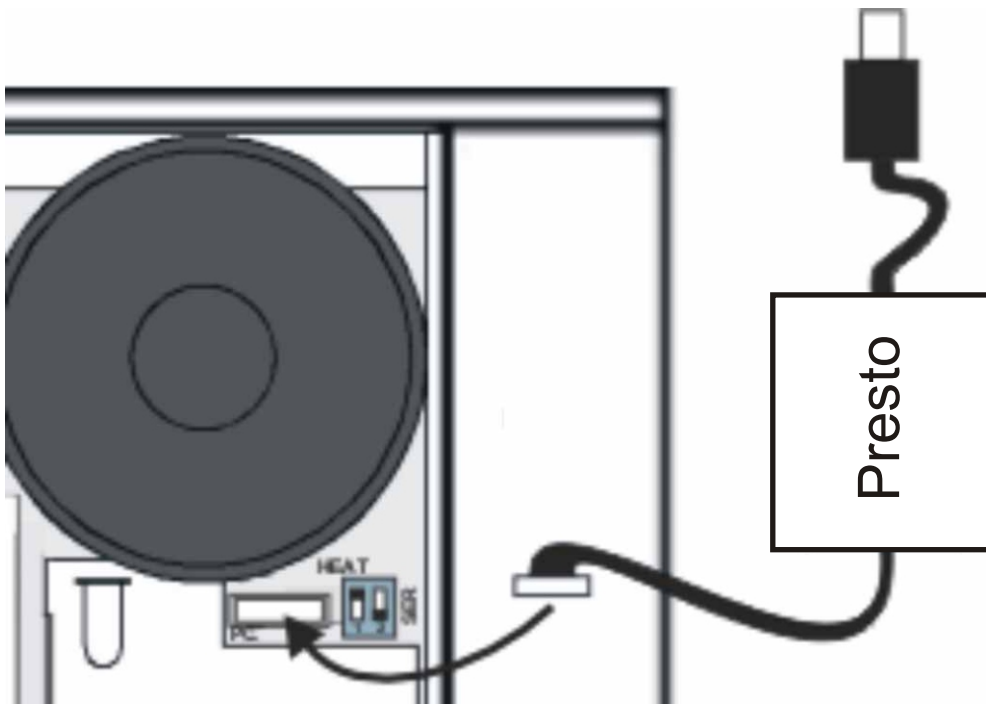
Fuses: CP CPD LVP BOREN

#PWRITE: Enabled

WRT: Writable WDTE

OSC: HS

ID: 3FFF 3FFF 3FFF 3FFF ...



Internal 5V approx. 5V
Active power supply

Programming process

60%

100%

Programming main memory

OK Cancel

Tasks Failures

- ✓ Device erase
- ✓ Blank check main memory
- ✓ Blank check data memory
- ✓ Blank check configuration word
- ✓ Blank check ID locations
- ✓ Programming data memory
- ✓ Verification data memory
- ▶ Programming main memory
 - Programming ID locations
 - Verification main memory
 - Verification ID locations
 - Programming configuration word
 - Verification configuration word